SiGe alloys have been widely integrated in large variety of devices such as CMOS for strain engineering, bipolar transistors, photonic detectors, bolometers, quantum cascade lasers and bio- or gas-sensors. For these applications, SiGe material is grown at low temperatures and in terms of Ge content, interfacial and layer quality. All multilayers were processed in form of mesa and the noise behavior of electrical signal was investigated by comparing the power spectral density curves and SNR values. The SiGe/Si multilayer structures were also characterized by the conventional material analysis tools and the results were compared to the noise measurements. The quality of SiGe/Si interface or SiGe layer was monitored by intentional exposure to oxygen in range of 2–1600 nTorr either during or prior to SiGe growth. The results demonstrated that SNR was sensitive to the interfacial and layer quality, and the Ge content in a multilayer structure. The noise level became very high when the strain fluctuated within SiGe layer and this occurred for SiGe with high Ge content or SiGe dots.

Experimental

The SiGe/Si multilayer structures were deposited on Si(100) substrates in temperature range of 350–650 °C at 20 torr using reduced pressure chemical vapor deposition (RPCVD). The reactant gases for SiGe growth were GeH4 or Ge2H6 and SiH4 or Si2H6 as Ge and pressure chemical vapor deposition (RPCVD). The reactant gases for Si sources, respectively. All SiGe layers were grown below the critical thickness in the metastable region. One problem with epitaxial growth at low temperatures is oxygen and moisture contamination which is rooted from epitaxy environment and the purity of epi-layers grown at low temperatures is oxygen and moisture contamination. The level of oxygen in the grown layers.

High-resolution reciprocal lattice mappings (HRRLM) were also performed to determine the misfit parameters and relaxation amount. The multilayers were sandwiched between highly B-doped layers with concentration of 5 × 10^{19} cm^{-3} for contacting. In order to avoid auto-doping or out-diffusion of boron into the SiGe/Si layers, Si spacers of 300 nm were grown on the top and bottom of multilayer structures as shown in Fig. 2. The mesas were formed by dry etching with areas of 70 × 70, 100 × 100, 140 × 140, and 200 × 200 μm². These mesas were passivated by 100 nm Si oxide. The contact areas were opened and Ni silicide was formed at 450 °C prior to metallization of Ti/W/Al. The purpose of Ni silicide layer is to reduce the contact resistance which may act as a noise source in the signal analysis.

The silicide process created mostly NiSi phase according to X-ray analysis. Higher annealing temperatures were avoided in order to minimize the intermixing of Si into Ge dots. The silicide layers were the thickness of the SiGe/Si multilayer structures with four periods (in blue color) and simulated curve (in red color). 20 and 30 stand for incident and diffracted angles, respectively.

Figure 1. X-ray rocking curve of Si_{0.72}Ge_{0.28}/Si multilayer structures with four periods (in blue color) and simulated curve (in red color). 20 and 30 stand for incident and diffracted angles, respectively.
located far from SiGe layers to ensure no strain relaxation occurs in SiGe/Si MQWs. The quality of SiGe layers was examined by introducing oxygen (2–1600 nTorr) at the interfaces of SiGe/Si. These samples were prepared to study the effect of interface quality on the electrical transport in the mesas. Figs. 3a–3c summarize the process steps and the designed structures in this study.

The noise measurements were performed by using a Cascade 11000 shielded probe station which is equipped with a battery powered resistive network together with a low-noise voltage pre-amplifier and a HP49810 vector spectrum analyzer. An appropriate applied voltage ($V_{bias}$) for the mesas could be in the range of 0.5 V to 5 V and in this study, $V_{bias} = 1$ V was chosen in order to avoid any undesired heat generation in the mesas. The power spectral density (PSD) of noise voltage was measured and compared for different types of SiGe/Si samples.

In general, $1/f$ noise is caused by interactions of carriers with impurities, imperfections in the crystal and surface states where a trapping/detrapping phenomenon occurs. In semiconductors, $1/f$ noise is usually expressed by noise constant according to:

$$K_{1/f} = \frac{\alpha_H}{N}$$  \[1\]

where $N$ stands for the total number of free charges and $\alpha_H$ is the Hooge’s constant.21

The current and voltage noise ($S_I$ and $S_V$, respectively) in SiGe/Si stack can be formulated as following:22

$$S_I = \frac{K_{1/f} I_{sample}^2}{f^\gamma} = \frac{K_{1/f} (V_{bias} / R_{sample})^2}{f^\gamma}$$  \[2\]

$$\Rightarrow S_V = (R_{sample})^2 S_I = \frac{K_{1/f} V_{bias}^2}{f^\gamma}$$  \[3\]

where $I_{sample}$ and $R_{sample}$ refer to the current and resistance of the device during the measurement, respectively. In these equations, the power of frequency, $\gamma$ is nearly 1. If the performance of $1/f$ noise deviates from value 1, this is mostly rooted from generation-recombination (GR) noise caused by defects in the SiGe/Si multilayer structure or at the interface of the oxide-passivated surface of the mesa body. The $K_{1/f}$ is acquired by the slope of the decaying part of the PSD curve.

Results and Discussion

SiGe material is usually deposited at low temperatures in order to have highly strained layers with precisely controlled thicknesses.
At low growth temperatures, the quality of process gases and epitaxy environment in terms of oxygen or moisture level is an issue.\textsuperscript{23–25} The source of this problem could be related to purity of the reactant gases (although the highest purity gases are used) and the presence of oxygen and moisture in the load-locks when the samples are loaded (usually no baking is performed). Although the background contamination level is minor and it could be in range of 2–10 nTorr during epitaxy according to RGA measurements but it still could affect the layer and interfacial quality of SiGe/Si.

Many semiconductor manufacturers use in-situ high-resolution X-ray diffractometer to examine the epi-layer quality. The instrument performs measurements of so-called rocking curves (RCs) when the incident angle is being scanned within an angle interval (usually around a symmetric reflection e.g. (004)) and the detector follows the diffracted beam to find out the layer and substrate peaks. The main criteria for RCs are the full-width-half-maximum (FWHM) of the layer peak and the number of interference fringes to find out the layer quality. In these measurements, a layer peak with small FWHM and many interference fringes are indications for a high quality of the epi-layer. In a rocking curve, the position of layer peak compared to the substrate peak determines the strain amount in the epi-layer. Figs. 4a and 4b show a series of RCs from SiGe layers grown at different temperatures and oxygen exposure levels during the growth. These RCs indicate no change of SiGe layers FWHM nor the number of fringes.

Therefore, no defects could be detected for SiGe layers grown with oxygen partial pressure up to 160 nTorr. This value corresponds to an oxygen concentration of \( \sim 1 \times 10^{19} \) cm\(^{-3} \) according to SIMS measurements.\textsuperscript{14} In fact, such a case may occur when a leakage incident occurs in the CVD reactor. The reason for such low resolution of X-ray rocking curves in Figs. 4a and 4b is due to the fact that these measurements are one-dimensional and the in-plane lattice parameter which is more sensitive to defects is not measured. Therefore, a two-dimensional analysis e.g. high-resolution reciprocal lattice mapping (HRRLM) is proposed as shown in Figs. 5a–5c. In all these maps, the integrity of the SiGe layers contours is preserved but a diffused scattering appears around the peak in Fig. 5b and Fig. 5c. Such diffused scattering is an indication of precipitates which cause a local disturbance in the SiGe matrix.\textsuperscript{18} The strain relaxation in these epi-layers was calculated from misfit parameters and it was below 1%.

Oxygen contamination at SiGe/Si interface forms oxide islands.\textsuperscript{25} Fig. 6 demonstrates the size of the formed oxide islands for all oxygen exposure experiments. A critical island size of 0.025 \( \mu \)m\(^2\) for oxide
islands has to be reached in order to see any effect on epitaxy quality. This could be determined from HRRLMs when SiGe layer lattice parameters could be determined and the initial strain relaxation could be observed in presence of defects. For each sample, the size of oxide islands was obtained from SEM images. This means that the layer quality degradation could be distinguished by characterization tools when the size of defects is large and the defect density is high.

Furthermore, two sample structures were processed with a minor amount of oxygen contamination (10 nTorr) at SiGe/Si interfaces (MQW2) or during the growth (MQW3). The background pressure of oxygen in the CVD reactor was measured to be 1 nTorr by RGA.

Fig. 7 shows the PSD curves of these samples and $K_{1/f}$ values are summarized in Table I. In this figure, MQW1 is grown without oxygen contamination and is considered as reference sample.

In these samples, the reference sample indicates the lowest noise level ($K_{1/f} = 7 \times 10^{-11}$). The noise level increases by a factor of 2 when the oxygen is within the grown layer ($K_{1/f} = 1.4 \times 10^{-10}$) meanwhile the noise level increases by a factor of 10 when the oxygen is present at the interface ($K_{1/f} = 8 \times 10^{-10}$).

This means that the transport of the carriers is very sensitive to the formed interfacial barriers (or defects) due to the oxygen islands formation. The first advice to the epitaxial growers is to avoid any long time interrupt during epitaxy (usually different purge steps are required) and to create growth recipes with fast purging.

The number of periods and the Ge content in SiGe/Si are also two important integration issues for device application. Both of these factors determine the total strain energy in the multilayer structure which should not exceed the energy of relaxation. Noise measurement provides an indication of the increase in defect density by any change of periodicity and the Ge content.

As an example, SiGe/Si multilayer structure is proposed as the active part for absorbing IR radiation in bolometers. For a high performance bolometer, a high temperature coefficient of resistance (TCR) and high SNR are two important criteria. Both of these criteria are dependent on the SiGe profile in the structure.

**Table I. Summary of oxygen exposure and $K_{1/f}$.

<table>
<thead>
<tr>
<th>Sample description</th>
<th>MQW1</th>
<th>MQW2</th>
<th>MQW3</th>
</tr>
</thead>
<tbody>
<tr>
<td>Oxygen exposure</td>
<td>No</td>
<td>Interfacial</td>
<td>Within SiGe layers</td>
</tr>
<tr>
<td>$K_{1/f}$ for $25 \times 25 \mu m^2$</td>
<td>$7 \times 10^{-11}$</td>
<td>$8 \times 10^{-10}$</td>
<td>$1 \times 10^{-10}$</td>
</tr>
</tbody>
</table>

In this study, three samples with multilayers of SiGe/Si were grown with Ge contents of 21%, 28% and 32% (MQW1, MQW2 and MQW3, respectively). Since the critical thickness of SiGe layers becomes impractically small for higher Ge contents, therefore SiGe-dots/Si multilayers were instead grown. The composition (or strain amount) in the Ge dots depends on the size and the growth temperature. The latter parameter is also the main factor for the intermixing of Si into the Ge material. In this study, the SiGe dots are made by intermixing of Si and Ge dots at 600°C. The X-ray analysis shows that the Ge content in MQD1 is ~47%. In order to reduce the intermixing, the growth temperature of Ge dots in MQD2 sample was decreased to 450°C. Together with a Si reference sample, there are a total of six samples in this experiment.

Although, it is believed the Ge content in MQD2 is higher than MQD1 but X-ray results showed almost similar strain amount in these samples. This could be realized when the strain relaxation in MQD2 occurs due to high Ge content.

Fig. 8 shows the PSD curves versus applied frequency for all MQWs and MQDs samples and the extracted $K_{1/f}$ values are summarized in Table II. In this figure, the highest noise level is registered for MQD1 and MQD2 samples meanwhile this increased in MQWs by the increase in Ge content. Unexpectedly, the behavior of SNR of MQDs samples was very similar, despite their different Ge content. One plausible reason could be the amount of lattice distortion in these samples has been comparable. This could occur because of the strain relaxation in Ge dots with higher Ge content in MQD2 which makes the resulted strain amount in the same range as in MQD1. Further TEM investigations are required to confirm this theory.

A more detailed analysis was performed by cross-sectional HRSEM to evaluate the layer quality of samples as shown in Figs. 9a–9c. The analysis shows that MQW1 and MQW2 are deposited through 2D growth (see flat layers in MQW1) whereas MQW3 shows a Sinus-wave like growth similar to MQD1 which trends 3D growth (or uneven layer and the growth follows Stranski-krastanov mode in Figs. 9b and 9c).

The reason behind the unevenness of SiGe layers in MQW3 is because the layer thickness is close to the critical thickness. It is worth mentioning here that the strain relaxation of thin strained layers may occur through roughness (or unevenness) of the layers and not creating the misfit dislocation.27,28

In this case, one may conclude that the noise behavior is sensitive to the strain fluctuation within the layers. This could generate scattering of carriers during the transport through the multilayers.23

More analysis was performed to investigate the strain distribution in the SiGe dots. Figs. 10a–10c show HRRLMs and the rocking curves from a series of calibrating samples with GeH4 exposure times of 0.5, 1, 2 and 4 minutes. The peaks from the Ge-dots in Figs. 10a and 10b are diffused but with a careful observation the maximum intensity...
could be located and the relaxation amount was determined to be 80%.

RCs in HRXRD are benefiting from very high X-ray intensity which is most necessary for these samples due to the small signal from Ge-dots. The RCs in Fig. 10c show the Ge peak shifts to the higher angles as the Ge-dots become larger. This is a sign of strain relaxation in Ge-dots. A full relaxation will occur when the dots in large scale coalesce (four minutes exposure). Therefore, it is most probable that a GeH4 exposure time shorter than one minute can provide the mostly strained dots (size of 3–10 nm).

Conclusions

SNR as a powerful tool to characterize engineered structures such as single crystalline dots or layers of SiGe/Si in multilayer structures has been presented and the results were compared with structural and chemical material analysis. Low temperature epitaxy growth suffers from the quality of process gases and epitaxy environment in terms of oxygen or moisture level. Therefore, the SiGe/Si interface or SiGe layer was examined by intentional exposure to oxygen in range of 2–1600 nTorr. No defects could be detected with oxygen partial pressures below 1%.

References


Table II. Summary of calculated $K_{1/f}$ for SiGe/Si MQWs and MQDs.

<table>
<thead>
<tr>
<th>Sample description</th>
<th>Ref. Si</th>
<th>MQW1</th>
<th>MQW2</th>
<th>MQW3</th>
<th>MQD1</th>
<th>MQD2</th>
</tr>
</thead>
<tbody>
<tr>
<td>Ge content</td>
<td>0</td>
<td>23</td>
<td>28</td>
<td>32</td>
<td>47</td>
<td>&gt;47</td>
</tr>
<tr>
<td>$K_{1/f}$ for 25 x 25 μm²</td>
<td>9 x 10⁻¹³</td>
<td>1 x 10⁻¹²</td>
<td>3 x 10⁻¹²</td>
<td>4.4 x 10⁻¹²</td>
<td>2 x 10⁻⁹</td>
<td>2 x 10⁻⁹</td>
</tr>
</tbody>
</table>

Figure 9. (a–c) Cross-section HRSEM micrographs from SiGe/Si multilayers with Ge contents of: (a) 28% and (b) 32% and (c) SiGe-dots with Ge content of 47%. $\theta$ and $2\theta$ stand for incident and diffracted angles, respectively.

Figure 10. (a–c) HRRLMs from Ge (or SiGe) dots with GeH4 exposure times of: (a) 4 min and (b) 2 min and (c) RC of samples with Ge (or SiGe) with different exposure times. SR-Ge stands for strain relaxed layer with thickness of 300 nm.