


7) Sharafinejad, Reza, Bijan Alizadehmalafeh, and . "UPF-based formal verification of low power techniques in modern processors." IEEE VLSI Test Symposium (VTS 2015),.


13) Beigmohammadi, Siamack, and Bijan Alizadehmalafeh. "Combinational Trace Signal Selection with Improved State Restoration for Post-Silicon Debug." DATE 2016,.

Bijan Alizadehmalafeh, and Masahiro Fujita. "Systematic Approximate Logic Optimization Using Don’t Care Conditions." ISQED,.


1) Mehri, Hossein, and Bijan Alizadehmalafeh. "An analytical dynamic and leakage power model for FPGAs." ICEE 2014,.